

A Primer on Cache Attacks

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Outline

- Mastik
- Flush + Reload
- Prime + Probe

Mastik

- **Micro-Architectural Side-channel ToolKit**
- <https://github.com/OxADE1A1DE/Mastik>
- Aims:
 - ▶ Collate information on side-channel attacks
 - ▶ Overcome the barrier to entry into the area
 - ▶ Shift focus to cryptanalysis

Mastik: status

- Reasonably solid implementation of four attacks:
 - ▶ Prime+Probe (L1-D, L1-I, L3)
 - ▶ Flush+Reload
 - ▶ Flush+Flush
 - ▶ CacheBleed
- Only Intel x86-64 on Linux
- Limited documentation and testing
- No user feedback

Computer Revolution

Processor speed

Memory latency

1977



1 MHz

500 ns

2020



20×3700 MHz

50 ns

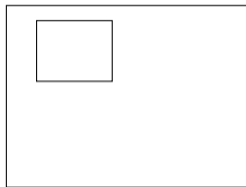
Bridging Speed Gap

- Utilize locality
 - ▶ spatial: divide memory into **lines**
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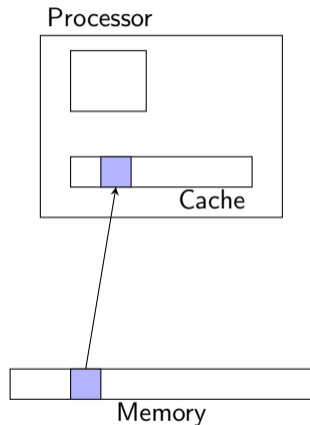
Processor



Memory

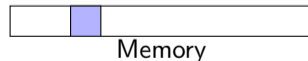
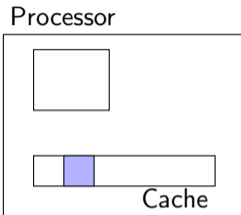
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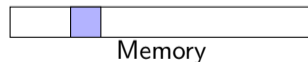
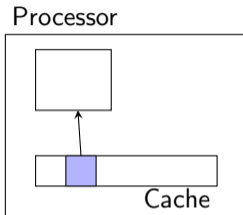
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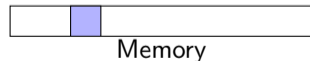
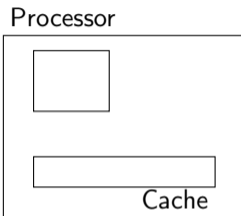
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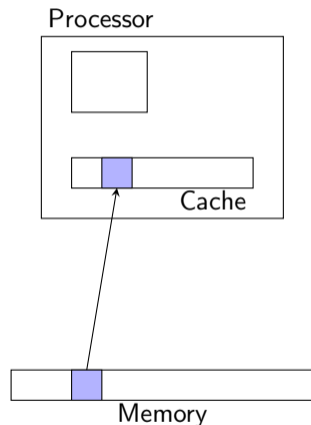
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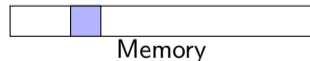
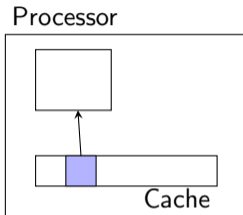
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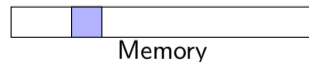
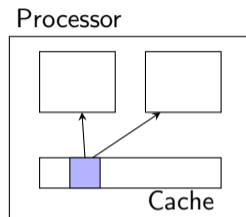
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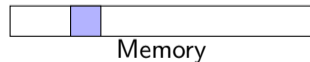
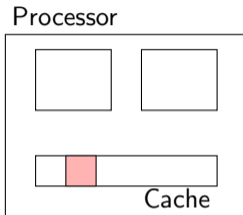
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- Share cache
 - ▶ improve performance of multi-core processors



Maintaining Consistency

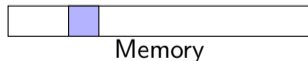
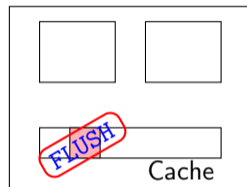
- Memory and cache can be inconsistent
 - ▶ rare but possible



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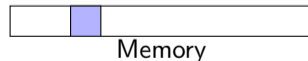
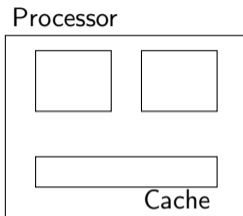
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- Solution: flush cache contents
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Processor



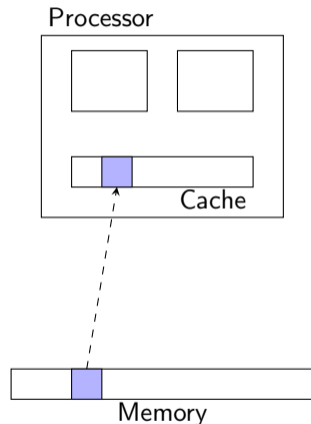
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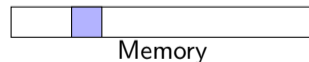
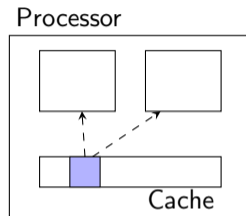
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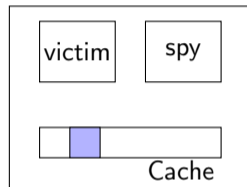
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Flush + Reload [GBK11, YF14]

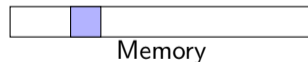
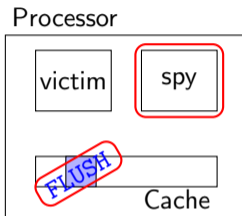
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 - ▶ slow → no victim access
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- (Repeat)

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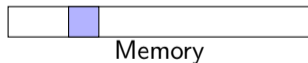
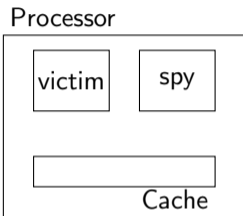
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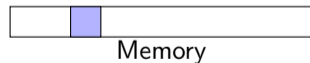
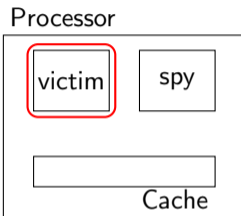
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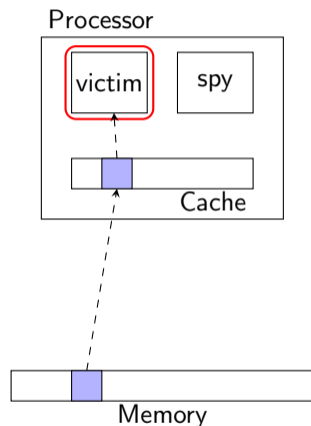
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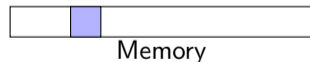
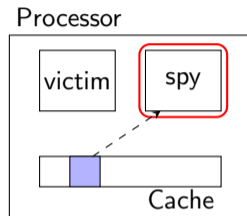
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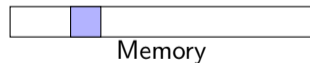
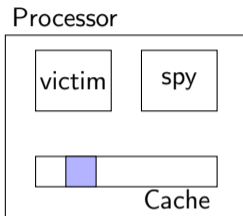
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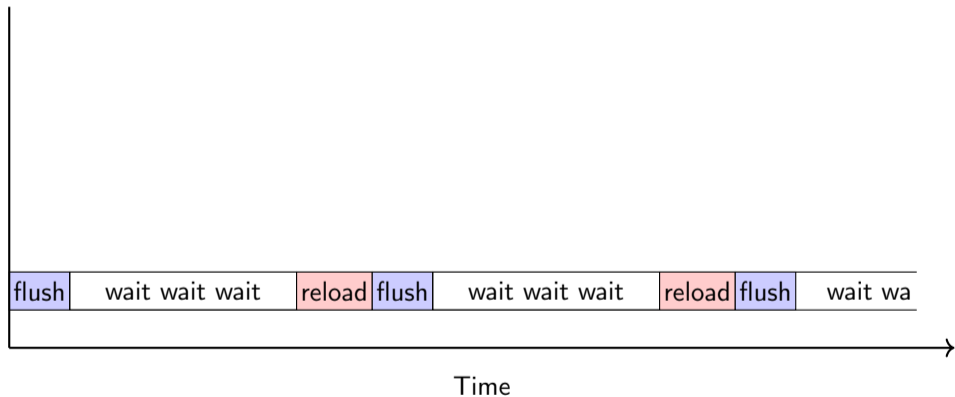
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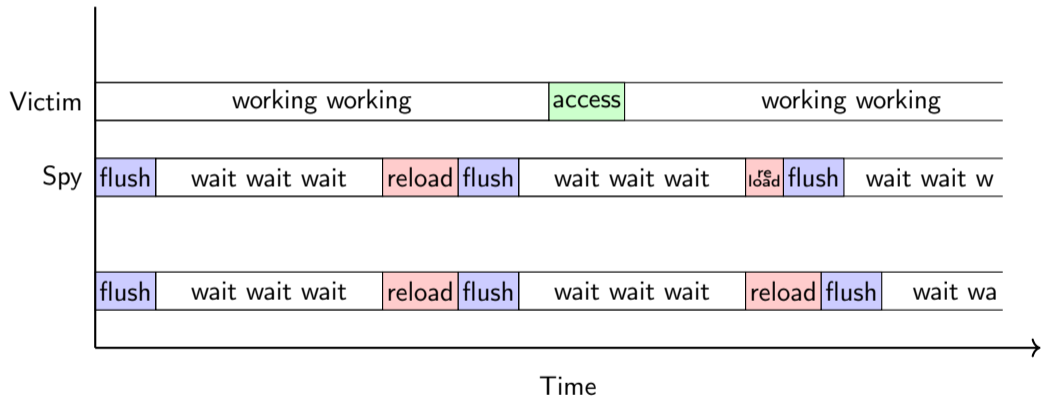
● (Repeat)



Flush + Reload: in action



Flush + Reload: in action



Flush + Reload: code

```
mfence           ; ensure in-order execution
rdtscp           ; get start time
mov %eax, %esi   ; store start time
mov (%ebx), %eax ; access memory
rdtscp           ; get finish time
sub %esi, %eax   ; subtract start and finish time
clflush 0 (%ebx) ; flush cache line
```

Flush + Reload: demo1

- FR-access

GnuPG 1.4.13 Modular Exponentiation

Compute $x = b^d \bmod n$

```
 $x \leftarrow 1$   
for  $i \leftarrow |d|-1$  downto 0 do  
   $x \leftarrow x^2 \bmod n$   
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Example: $b=11$, $d=5=101_2$, $n=100$

$$\begin{aligned} & 11^5 \bmod 100 \\ &= 161051 \bmod 100 \\ &= 51 \end{aligned}$$

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	1	2	101

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Square	1	2	101

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Square	1	2	101
Reduce	1	2	101

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Operation	x	i	d_i
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Square	1	2	101
Reduce	1	2	101
Multiply	11	2	101

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Reduce	1	2	101
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Reduce	11	2	101
Square	121	1	101

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Reduce	21	1	101
Square	441	0	101

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Multiply	451	0	101

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Reduce	41	0	101
Multiply	451	0	101
Reduce	51	0	101

Flush + Reload: demo

- FR-gnupg-1.4.13

Flush + Reload: pros & cons

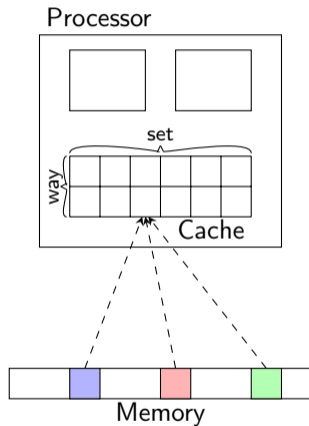
- + Simple
- + Very few false positive
- + Resolution of memory line

Flush + Reload: pros & cons

- + Simple
- + Very few false positive
- + Resolution of memory line
 - Only work with shared memory
 - Require flush instruction

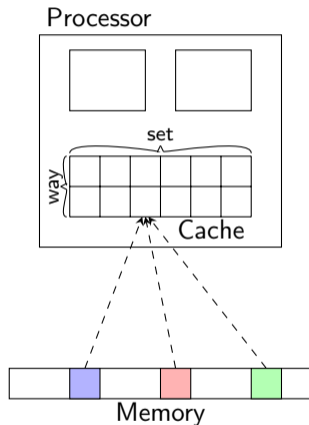
Set Associative Caches

- Memory lines map to **cache sets**



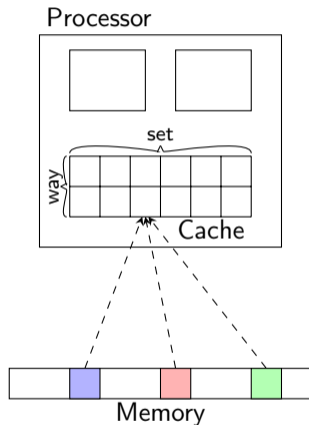
Set Associative Caches

- Memory lines map to **cache sets**
- Each line maps to one particular set and can be stored in any of its ways



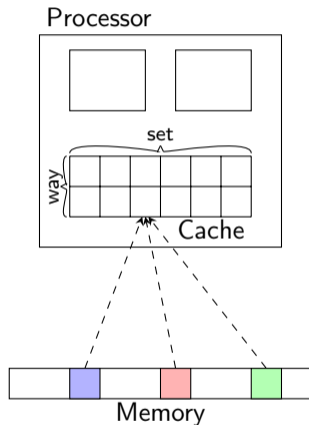
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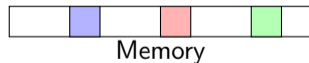
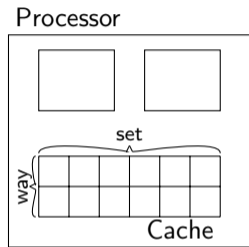
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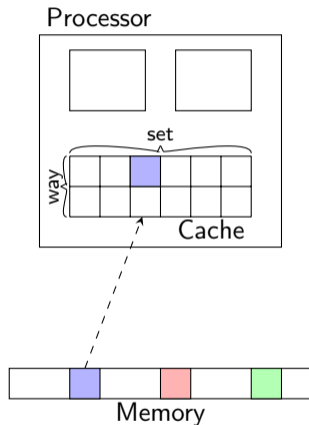
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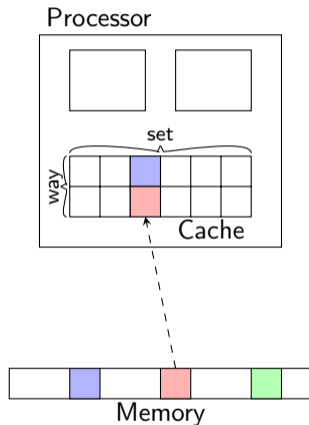
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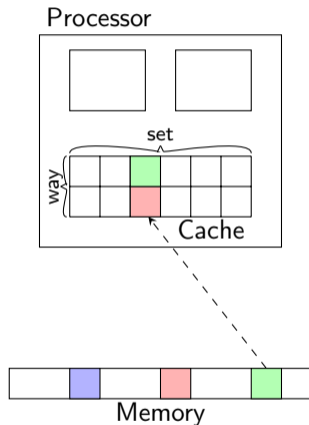
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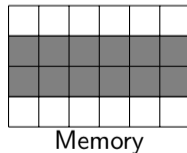
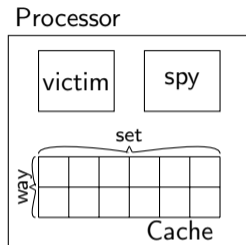
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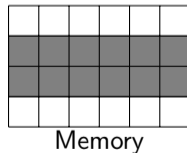
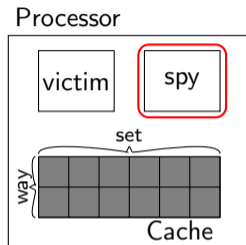
Prime + Probe [OST05, Per05]

- (Allocate cache-sized memory buffer)
- **Prime**: access all lines in buffer
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- Wait (victim executes)
→ may evict some cache lines
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 - ▶ slow → cache line evicted
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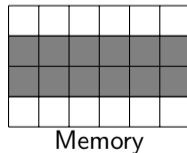
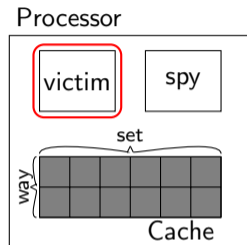
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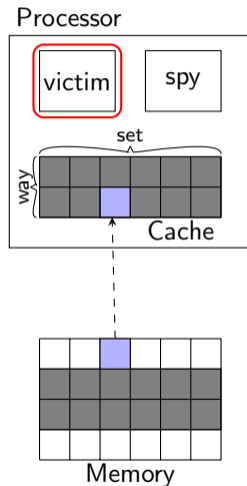
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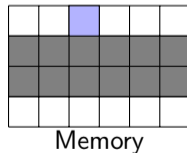
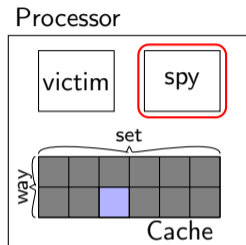
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Prime + Probe: sample victim data rattle

```
volatile char buffer[4096];

int main(int ac, char **av) {
    for (;;) {
        for (int i=0; i<64000; i++)
            buffer[800] += i;

        for (int i=0; i<64000; i++)
            buffer[1800] += i;
    }
}
```

Prime + Probe: demo

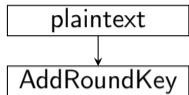
- Cache finger print of the rattle program

Prime + Probe: attack first-round AES

plaintext

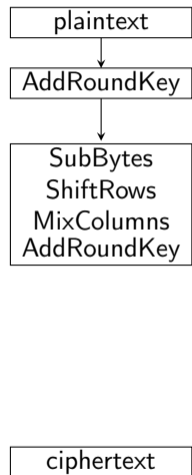
ciphertext

Prime + Probe: attack first-round AES

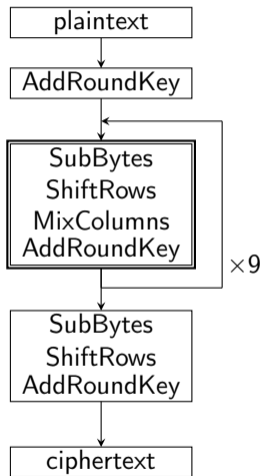


ciphertext

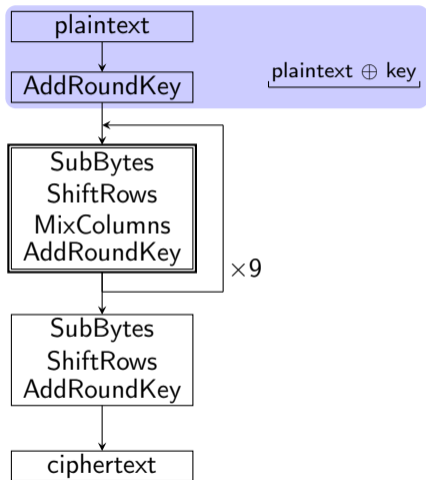
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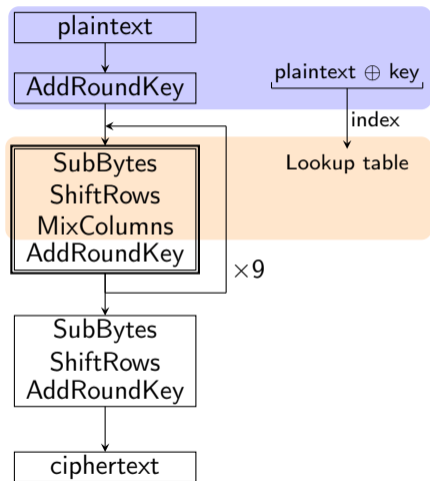
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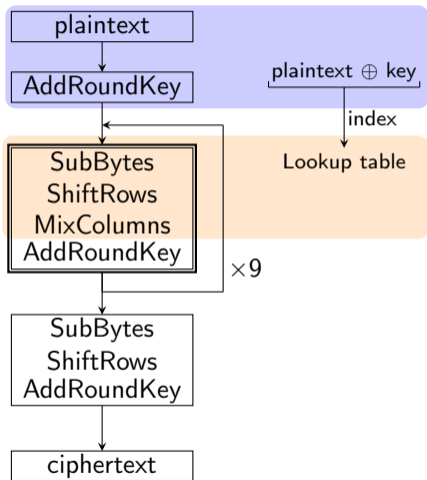
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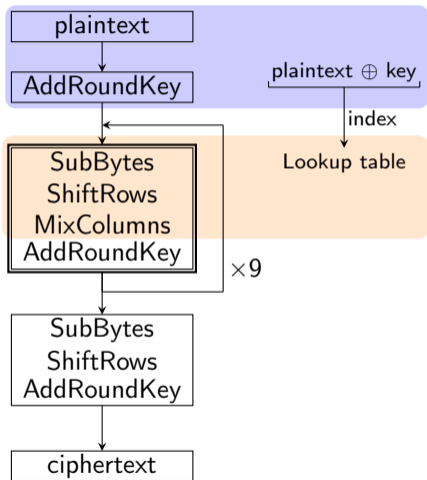


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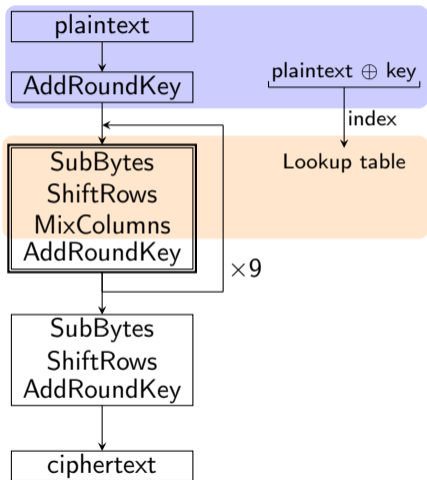
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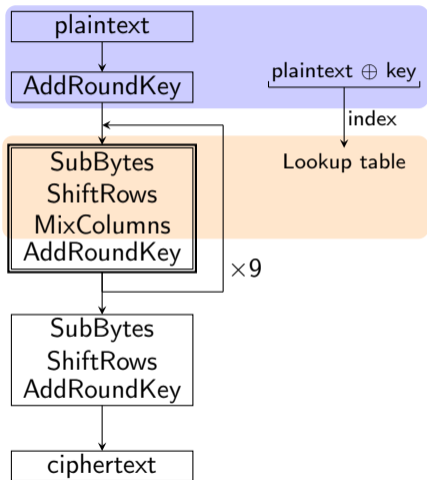
- (Assume a cache with 64 sets)
- A table has 256 entries, each of size 4 bytes

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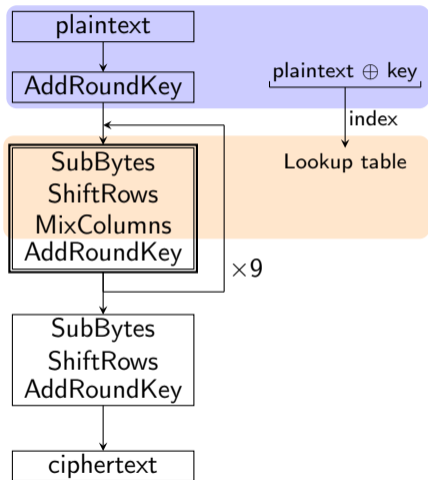
- (Assume a cache with 64 sets)
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→ occupy $\frac{256 \times 4}{64} = 16$ cache lines

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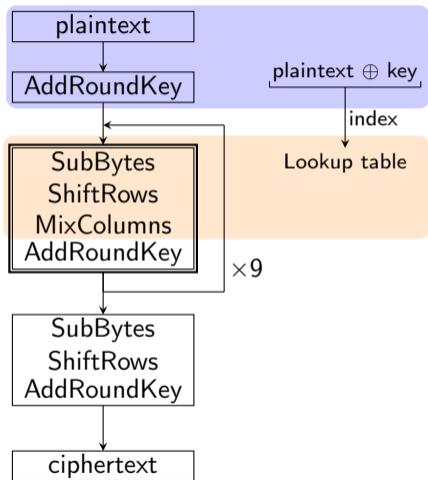
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→ eliminate impossible keys to one cache set

Prime + Probe: attack first-round AES (cont.)

- Actual experiments have noise

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Prime + Probe: attack first-round AES (cont.)

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 - sometimes cannot distinguish cache hit/miss
(always show that all sets are accessed)
- Average access time per cache set
 - ▶ p_μ : average of all n samples
 - ▶ p_{μ_0} : average of samples whose 4 MSBs of the plaintext byte are 0000
 - ▶ p_{μ_1} : average of samples whose 4 MSBs of the plaintext byte are 0001
 - ▶ ...
 - ▶ $p_{\mu_{15}}$: average of samples whose 4 MSBs of the plaintext byte are 1111

Prime + Probe: attack first-round AES (cont.)

$$pt_0 = [t_0^0, t_0^1, \dots, t_0^{63}]$$

Prime + Probe: attack first-round AES (cont.)

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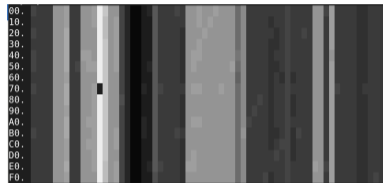
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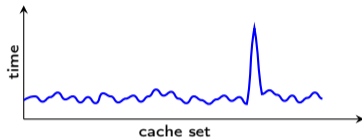
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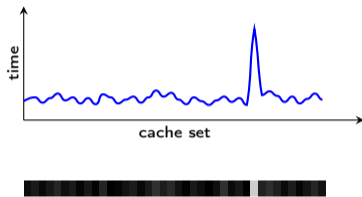
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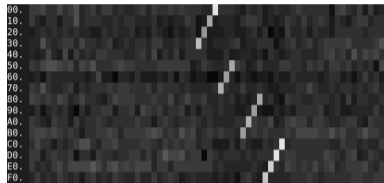
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Prime + Probe: attack first-round AES (cont.)

$$\begin{aligned} p_{t_0} &= [t_0^0, t_0^1, \dots, t_0^{63}] \\ p_{t_1} &= [t_1^0, t_1^1, \dots, t_1^{63}] \\ &\dots \\ p_{t_n} &= [t_n^0, t_n^1, \dots, t_n^{63}] \end{aligned}$$



$$\begin{aligned} p_{\mu} &= [\text{avg}_n(t^0), \text{avg}_n(t^1), \dots, \text{avg}_n(t^{63})] \\ p_{\mu_0} &= [\text{avg}_0(t^0), \text{avg}_0(t^1), \dots, \text{avg}_0(t^{63})] \\ p_{\mu_1} &= [\text{avg}_1(t^0), \text{avg}_1(t^1), \dots, \text{avg}_1(t^{63})] \\ &\dots \\ p_{\mu_{15}} &= [\text{avg}_{15}(t^0), \text{avg}_{15}(t^1), \dots, \text{avg}_{15}(t^{63})] \end{aligned}$$

$$\tilde{p}_{\mu_0} = p_{\mu_0} - p_{\mu}$$

$$\tilde{p}_{\mu_1} = p_{\mu_1} - p_{\mu}$$

$$\tilde{p}_{\mu_{15}} = p_{\mu_{15}} - p_{\mu}$$

Prime + Probe: demo

- First-round AES

Prime + Probe: attack first-round AES (cont.)

- Recall Prime + Probe has a resolution of cache set
→ detect which cache set is (not) accessed

Prime + Probe: attack first-round AES (cont.)

- Recall Prime + Probe has a resolution of cache set
 - detect which cache set is (not) accessed
- A table spans over $16 = 2^4$ sets
 - reveal top 4 bits at best

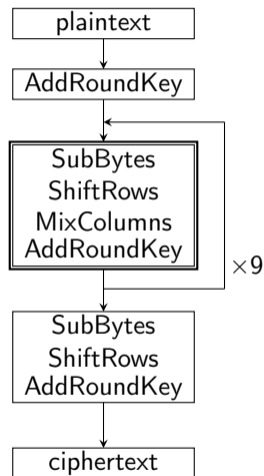
Prime + Probe: attack first-round AES (cont.)

- Recall Prime + Probe has a resolution of cache set
→ detect which cache set is (not) accessed
- A table spans over $16 = 2^4$ sets
→ reveal top 4 bits at best
- To recover the remaining bits
 - ▶ combine with second-round cryptanalysis
 - ▶ attack final round instead
 - ▶ etc.

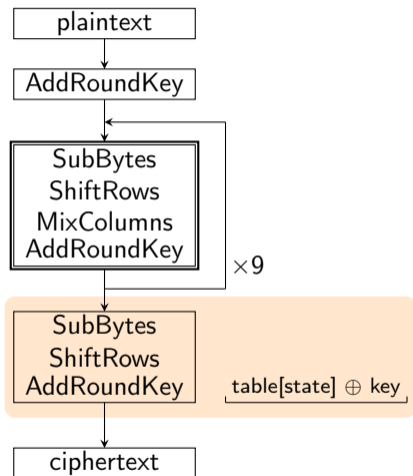
Prime + Probe: attack first-round AES (cont.)

- Recall Prime + Probe has a resolution of cache set
→ detect which cache set is (not) accessed
- A table spans over $16 = 2^4$ sets
→ reveal top 4 bits at best
- To recover the remaining bits
 - ▶ combine with second-round cryptanalysis
 - ▶ **attack final round instead**
 - ▶ etc.

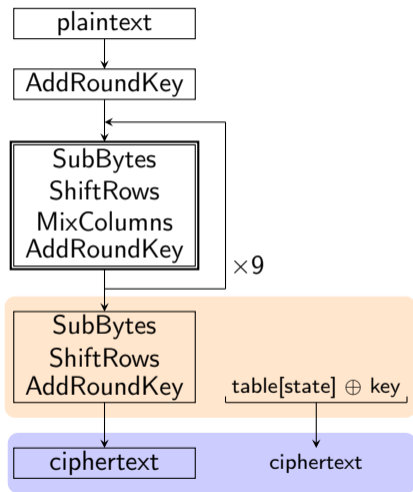
Prime + Probe: attack final-round AES



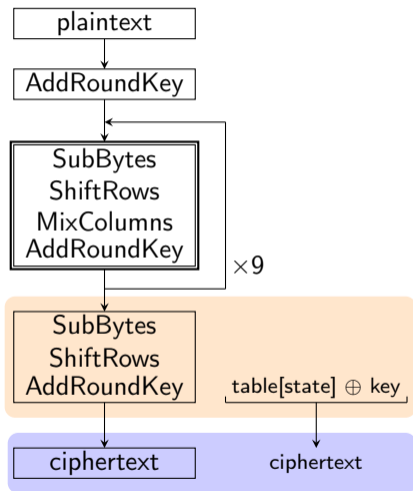
Prime + Probe: attack final-round AES



Prime + Probe: attack final-round AES

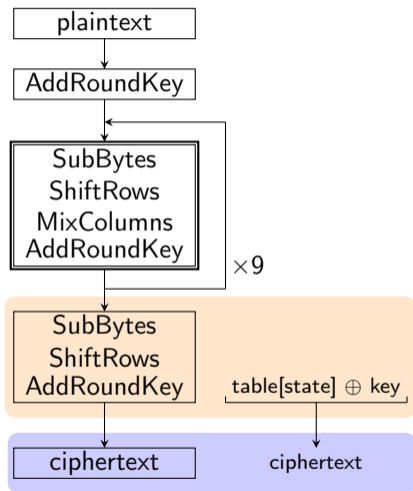


Prime + Probe: attack final-round AES



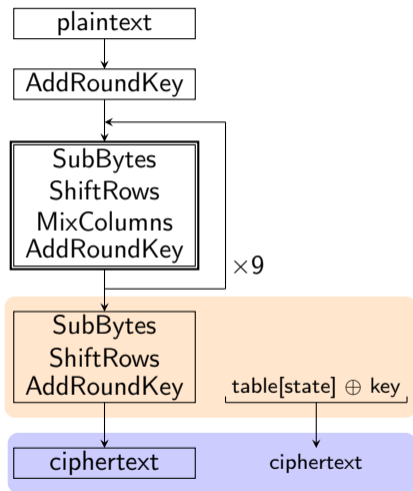
- Know ciphertext + guess key \rightarrow table index

Prime + Probe: attack final-round AES



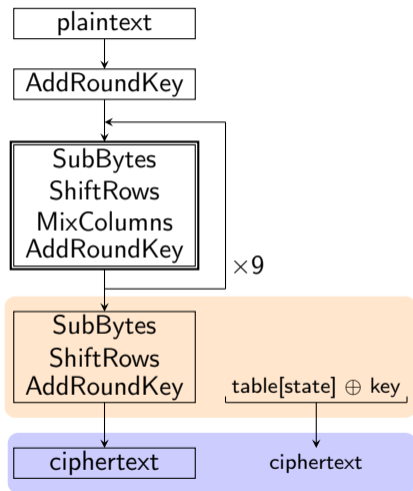
- Know ciphertext + guess key \rightarrow table index
- Exploit SBox confusion property

Prime + Probe: attack final-round AES



- Know ciphertext + guess key \rightarrow table index
- Exploit SBox confusion property
- Compute correlation between known and guessed key
 - high correlation indicates correct key guess

Prime + Probe: attack final-round AES



- Know ciphertext + guess key \rightarrow table index
- Exploit SBox confusion property
- Compute correlation between known and guessed key
 - high correlation indicates correct key guess
- Recover full key

Prime + Probe: attack final-round AES

$$p_{t_0} = [t_0^0, t_0^1, \dots, t_0^{16}, t_0^{17}, \dots, t_0^{63}] \begin{array}{l} \uparrow \\ \text{---} \\ \text{---} \end{array}$$

$$p_{t_1} = [t_1^0, t_1^1, \dots, t_1^{16}, t_1^{17}, \dots, t_1^{63}] \begin{array}{l} \uparrow \\ \text{---} \\ \text{---} \end{array}$$

...

$$p_{t_n} = [t_n^0, t_n^1, \dots, t_n^{16}, t_n^{17}, \dots, t_n^{63}] \begin{array}{l} \uparrow \\ \text{---} \\ \text{---} \end{array}$$

Prime + Probe: attack final-round AES

$$p_{t_0} = [t_0^0, t_0^1, \dots, t_0^{16}, t_0^{17}, \dots, t_0^{63}] \begin{array}{c} \uparrow \\ \text{---} \\ \text{---} \\ \text{---} \end{array}$$

$$p_{t_1} = [t_1^0, t_1^1, \dots, t_1^{16}, t_1^{17}, \dots, t_1^{63}] \begin{array}{c} \uparrow \\ \text{---} \\ \text{---} \\ \text{---} \end{array}$$

...

$$p_{t_n} = [t_n^0, t_n^1, \dots, t_n^{16}, t_n^{17}, \dots, t_n^{63}] \begin{array}{c} \uparrow \\ \text{---} \\ \text{---} \\ \text{---} \end{array}$$

$$k_{t_{00}} = [0, 15, \dots, -1, -1, \dots, 0] \begin{array}{c} \uparrow \\ \text{---} \\ \text{---} \\ \text{---} \end{array}$$

Prime + Probe: attack final-round AES

$$p_{t_0} = [t_0^0, t_0^1, \dots, t_0^{16}, t_0^{17}, \dots, t_0^{63}] \begin{array}{c} \uparrow \\ \text{---} \\ \text{---} \end{array}$$

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Prime + Probe: attack final-round AES

$$p_{t_0} = [t_0^0, t_0^1, \dots, t_0^{16}, t_0^{17}, \dots, t_0^{63}] \begin{array}{c} \uparrow \\ \text{---} \\ \text{---} \end{array}$$

$$p_{t_1} = [t_1^0, t_1^1, \dots, t_1^{16}, t_1^{17}, \dots, t_1^{63}] \begin{array}{c} \uparrow \\ \text{---} \\ \text{---} \end{array}$$

...

$$p_{t_n} = [t_n^0, t_n^1, \dots, t_n^{16}, t_n^{17}, \dots, t_n^{63}] \begin{array}{c} \uparrow \\ \text{---} \\ \text{---} \end{array}$$

$$k_{t_{00}} = [0, 15, \dots, -1, -1, \dots, 0] \begin{array}{c} \uparrow \\ \text{---} \\ \text{---} \end{array}$$

$$k_{t_{01}} = [0, -1, \dots, -1, 15, \dots, 0] \begin{array}{c} \uparrow \\ \text{---} \\ \text{---} \end{array}$$

$$k_{t_{02}} = [0, -1, \dots, -1, -1, \dots, 0] \begin{array}{c} \uparrow \\ \text{---} \\ \text{---} \end{array}$$

Prime + Probe: attack final-round AES

$$p_{t_0} = [t_0^0, t_0^1, \dots, t_0^{16}, t_0^{17}, \dots, t_0^{63}] \begin{array}{c} \uparrow \\ \text{---} \\ \text{---} \end{array}$$

$$p_{t_1} = [t_1^0, t_1^1, \dots, t_1^{16}, t_1^{17}, \dots, t_1^{63}] \begin{array}{c} \uparrow \\ \text{---} \\ \text{---} \end{array}$$

...

$$p_{t_n} = [t_n^0, t_n^1, \dots, t_n^{16}, t_n^{17}, \dots, t_n^{63}] \begin{array}{c} \uparrow \\ \text{---} \\ \text{---} \end{array}$$

$$k_{t_{00}} = [0, 15, \dots, -1, -1, \dots, 0] \begin{array}{c} \uparrow \\ \text{---} \\ \text{---} \end{array}$$

$$k_{t_{01}} = [0, -1, \dots, -1, 15, \dots, 0] \begin{array}{c} \uparrow \\ \text{---} \\ \text{---} \end{array}$$

$$k_{t_{02}} = [0, -1, \dots, -1, -1, \dots, 0] \begin{array}{c} \uparrow \\ \text{---} \\ \text{---} \end{array}$$

...

$$k_{t_{FF}} = [0, -1, \dots, 15, -1, \dots, 0] \begin{array}{c} \uparrow \\ \text{---} \\ \text{---} \end{array}$$

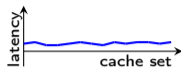
Prime + Probe: attack final-round AES

$$p_{t_0} = [t_0^0, t_0^1, \dots, t_0^{16}, t_0^{17}, \dots, t_0^{63}]$$

$$p_{t_1} = [t_1^0, t_1^1, \dots, t_1^{16}, t_1^{17}, \dots, t_1^{63}]$$

...

$$p_{t_n} = [t_n^0, t_n^1, \dots, t_n^{16}, t_n^{17}, \dots, t_n^{63}]$$



$$\tilde{p}_{t_{0,00}} = p_{t_0} \odot k_{t_0}$$

$$k_{t_{00}} = [0, 15, \dots, -1, -1, \dots, 0]$$

$$k_{t_{01}} = [0, -1, \dots, -1, 15, \dots, 0]$$

$$k_{t_{02}} = [0, -1, \dots, -1, -1, \dots, 0]$$

...

$$k_{t_{FF}} = [0, -1, \dots, 15, -1, \dots, 0]$$

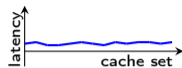
Prime + Probe: attack final-round AES

$$p_{t_0} = [t_0^0, t_0^1, \dots, t_0^{16}, t_0^{17}, \dots, t_0^{63}] \uparrow \text{latency}$$

$$p_{t_1} = [t_1^0, t_1^1, \dots, t_1^{16}, t_1^{17}, \dots, t_1^{63}] \uparrow \text{latency}$$

...

$$p_{t_n} = [t_n^0, t_n^1, \dots, t_n^{16}, t_n^{17}, \dots, t_n^{63}] \uparrow \text{latency}$$



$$\tilde{p}_{t_0,00} = p_{t_0} \odot k_{t_0}$$

$$\tilde{p}_{t_1,00} = p_{t_1} \odot k_{t_0}$$

$$k_{t_{00}} = [0, 15, \dots, -1, -1, \dots, 0] \uparrow \text{latency}$$


$$k_{t_{01}} = [0, -1, \dots, -1, 15, \dots, 0] \uparrow \text{latency}$$


$$k_{t_{02}} = [0, -1, \dots, -1, -1, \dots, 0] \uparrow \text{latency}$$

...


$$k_{t_{FF}} = [0, -1, \dots, 15, -1, \dots, 0] \uparrow \text{latency}$$


Prime + Probe: attack final-round AES


$$p_{t_0} = [t_0^0, t_0^1, \dots, t_0^{16}, t_0^{17}, \dots, t_0^{63}] \uparrow$$



$$p_{t_1} = [t_1^0, t_1^1, \dots, t_1^{16}, t_1^{17}, \dots, t_1^{63}] \uparrow$$


...


$$p_{t_n} = [t_n^0, t_n^1, \dots, t_n^{16}, t_n^{17}, \dots, t_n^{63}] \uparrow$$


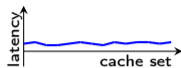
$$k_{t_{00}} = [0, 15, \dots, -1, -1, \dots, 0] \uparrow$$


$$k_{t_{01}} = [0, -1, \dots, -1, 15, \dots, 0] \uparrow$$


$$k_{t_{02}} = [0, -1, \dots, -1, -1, \dots, 0] \uparrow$$


...

$$k_{t_{FF}} = [0, -1, \dots, 15, -1, \dots, 0] \uparrow$$




$$\tilde{p}_{t_0,00} = p_{t_0} \odot k_{t_0}$$

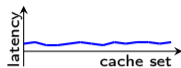
$$\tilde{p}_{t_1,00} = p_{t_1} \odot k_{t_0}$$

...

$$\tilde{p}_{t_n,00} = p_{t_n} \odot k_{t_0}$$

Prime + Probe: attack final-round AES

$$\begin{aligned}
 p_{t_0} &= [t_0^0, t_0^1, \dots, t_0^{16}, t_0^{17}, \dots, t_0^{63}] \uparrow \text{[latency plot]} \\
 p_{t_1} &= [t_1^0, t_1^1, \dots, t_1^{16}, t_1^{17}, \dots, t_1^{63}] \uparrow \text{[latency plot]} \\
 &\dots \\
 p_{t_n} &= [t_n^0, t_n^1, \dots, t_n^{16}, t_n^{17}, \dots, t_n^{63}] \uparrow \text{[latency plot]}
 \end{aligned}$$



$$\begin{aligned}
 \tilde{p}_{t_{0,00}} &= p_{t_0} \odot k_{t_0} \\
 \tilde{p}_{t_{1,00}} &= p_{t_1} \odot k_{t_0} \\
 &\dots \\
 \tilde{p}_{t_{n,00}} &= p_{t_n} \odot k_{t_0}
 \end{aligned}$$

$$\begin{aligned}
 \tilde{p}_{\mu_0} &= \text{sum}(\tilde{p}_{t_0, \cdot}) \text{ [latency plot]} \\
 \rho_0 &= \sum \tilde{p}_{\mu_0} \cdot
 \end{aligned}$$

$$\begin{aligned}
 k_{t_{00}} &= [0, 15, \dots, -1, -1, \dots, 0] \uparrow \text{[latency plot]} \\
 k_{t_{01}} &= [0, -1, \dots, -1, 15, \dots, 0] \uparrow \text{[latency plot]} \\
 k_{t_{02}} &= [0, -1, \dots, -1, -1, \dots, 0] \uparrow \text{[latency plot]} \\
 &\dots \\
 k_{t_{FF}} &= [0, -1, \dots, 15, -1, \dots, 0] \uparrow \text{[latency plot]}
 \end{aligned}$$

Prime + Probe: attack final-round AES

$$p_{t_0} = [t_0^0, t_0^1, \dots, t_0^{16}, t_0^{17}, \dots, t_0^{63}] \uparrow \text{latency}$$

$$p_{t_1} = [t_1^0, t_1^1, \dots, t_1^{16}, t_1^{17}, \dots, t_1^{63}] \uparrow \text{latency}$$

...

$$p_{t_n} = [t_n^0, t_n^1, \dots, t_n^{16}, t_n^{17}, \dots, t_n^{63}] \uparrow \text{latency}$$

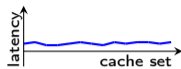
$$k_{t_{00}} = [0, 15, \dots, -1, -1, \dots, 0] \uparrow \text{latency}$$

$$k_{t_{01}} = [0, -1, \dots, -1, 15, \dots, 0] \uparrow \text{latency}$$

$$k_{t_{02}} = [0, -1, \dots, -1, -1, \dots, 0] \uparrow \text{latency}$$

...

$$k_{t_{FF}} = [0, -1, \dots, 15, -1, \dots, 0] \uparrow \text{latency}$$



$$\tilde{p}_{t_{0,00}} = p_{t_0} \odot k_{t_0}$$

$$\tilde{p}_{t_{1,00}} = p_{t_1} \odot k_{t_0}$$

...

$$\tilde{p}_{t_{n,00}} = p_{t_n} \odot k_{t_0}$$

$$\tilde{p}_{t_{0,01}} = p_{t_0} \odot k_{t_1}$$

$$\tilde{p}_{\mu_0} = \text{sum}(\tilde{p}_{t_0, \cdot})$$

$$\rho_0 = \sum \tilde{p}_{\mu_0}$$

Prime + Probe: attack final-round AES

$$p_{t_0} = [t_0^0, t_0^1, \dots, t_0^{16}, t_0^{17}, \dots, t_0^{63}] \uparrow \text{latency}$$

$$p_{t_1} = [t_1^0, t_1^1, \dots, t_1^{16}, t_1^{17}, \dots, t_1^{63}] \uparrow \text{latency}$$

...

$$p_{t_n} = [t_n^0, t_n^1, \dots, t_n^{16}, t_n^{17}, \dots, t_n^{63}] \uparrow \text{latency}$$

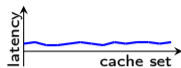
$$k_{t_{00}} = [0, 15, \dots, -1, -1, \dots, 0] \uparrow \text{latency}$$

$$k_{t_{01}} = [0, -1, \dots, -1, 15, \dots, 0] \uparrow \text{latency}$$

$$k_{t_{02}} = [0, -1, \dots, -1, -1, \dots, 0] \uparrow \text{latency}$$

...

$$k_{t_{FF}} = [0, -1, \dots, 15, -1, \dots, 0] \uparrow \text{latency}$$



$$\tilde{p}_{t_{0,00}} = p_{t_0} \odot k_{t_0}$$

$$\tilde{p}_{t_{1,00}} = p_{t_1} \odot k_{t_0}$$

...

$$\tilde{p}_{t_{n,00}} = p_{t_n} \odot k_{t_0}$$

$$\tilde{p}_{t_{0,01}} = p_{t_0} \odot k_{t_1}$$

$$\tilde{p}_{t_{1,01}} = p_{t_1} \odot k_{t_1}$$

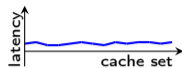
$$\tilde{p}_{\mu_0} = \text{sum}(\tilde{p}_{t_0, \cdot})$$

$$\rho_0 = \sum \tilde{p}_{\mu_0}$$

Prime + Probe: attack final-round AES

$$\begin{aligned}
 p_{t_0} &= [t_0^0, t_0^1, \dots, t_0^{16}, t_0^{17}, \dots, t_0^{63}] \uparrow \text{[latency plot]} \\
 p_{t_1} &= [t_1^0, t_1^1, \dots, t_1^{16}, t_1^{17}, \dots, t_1^{63}] \uparrow \text{[latency plot]} \\
 &\dots \\
 p_{t_n} &= [t_n^0, t_n^1, \dots, t_n^{16}, t_n^{17}, \dots, t_n^{63}] \uparrow \text{[latency plot]}
 \end{aligned}$$

$$\begin{aligned}
 k_{t_{00}} &= [0, 15, \dots, -1, -1, \dots, 0] \uparrow \text{[latency plot]} \\
 k_{t_{01}} &= [0, -1, \dots, -1, 15, \dots, 0] \uparrow \text{[latency plot]} \\
 k_{t_{02}} &= [0, -1, \dots, -1, -1, \dots, 0] \uparrow \text{[latency plot]} \\
 &\dots \\
 k_{t_{FF}} &= [0, -1, \dots, 15, -1, \dots, 0] \uparrow \text{[latency plot]}
 \end{aligned}$$



$$\begin{aligned}
 \tilde{p}_{t_{0,00}} &= p_{t_0} \odot k_{t_0} \\
 \tilde{p}_{t_{1,00}} &= p_{t_1} \odot k_{t_0} \\
 &\dots \\
 \tilde{p}_{t_{n,00}} &= p_{t_n} \odot k_{t_0}
 \end{aligned}$$

$$\begin{aligned}
 \tilde{p}_{t_{0,01}} &= p_{t_0} \odot k_{t_1} \\
 \tilde{p}_{t_{1,01}} &= p_{t_1} \odot k_{t_1} \\
 &\dots
 \end{aligned}$$

$$\begin{aligned}
 \tilde{p}_{\mu_0} &= \text{sum}(\tilde{p}_{t_0, \cdot}) \text{ [latency plot]} \\
 \rho_0 &= \sum \tilde{p}_{\mu_0} \cdot
 \end{aligned}$$

$$\begin{aligned}
 \tilde{p}_{\mu_1} &= \text{sum}(\tilde{p}_{t_1, \cdot}) \\
 \rho_1 &= \sum \tilde{p}_{\mu_1}
 \end{aligned}$$

Prime + Probe: attack final-round AES

$$p_{t_0} = [t_0^0, t_0^1, \dots, t_0^{16}, t_0^{17}, \dots, t_0^{63}] \uparrow \text{latency}$$

$$p_{t_1} = [t_1^0, t_1^1, \dots, t_1^{16}, t_1^{17}, \dots, t_1^{63}] \uparrow \text{latency}$$

...

$$p_{t_n} = [t_n^0, t_n^1, \dots, t_n^{16}, t_n^{17}, \dots, t_n^{63}] \uparrow \text{latency}$$

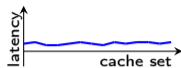
$$k_{t_{00}} = [0, 15, \dots, -1, -1, \dots, 0] \uparrow \text{latency}$$

$$k_{t_{01}} = [0, -1, \dots, -1, 15, \dots, 0] \uparrow \text{latency}$$

$$k_{t_{02}} = [0, -1, \dots, -1, -1, \dots, 0] \uparrow \text{latency}$$

...

$$k_{t_{FF}} = [0, -1, \dots, 15, -1, \dots, 0] \uparrow \text{latency}$$



$$\tilde{p}_{t_{0,00}} = p_{t_0} \odot k_{t_0}$$

$$\tilde{p}_{t_{1,00}} = p_{t_1} \odot k_{t_0}$$

...

$$\tilde{p}_{t_{n,00}} = p_{t_n} \odot k_{t_0}$$

$$\tilde{p}_{t_{0,01}} = p_{t_0} \odot k_{t_1}$$

$$\tilde{p}_{t_{1,01}} = p_{t_1} \odot k_{t_1}$$

...

...

...

$$\tilde{p}_{t_{n,FF}} = p_{t_n} \odot k_{t_{FF}}$$


$$\tilde{p}_{\mu_0} = \text{sum}(\tilde{p}_{t_0, \cdot})$$


$$\rho_0 = \sum \tilde{p}_{\mu_0}$$

$$\tilde{p}_{\mu_1} = \text{sum}(\tilde{p}_{t_1, \cdot})$$


$$\rho_1 = \sum \tilde{p}_{\mu_1}$$


Prime + Probe: attack final-round AES


$$p_{t_0} = [t_0^0, t_0^1, \dots, t_0^{16}, t_0^{17}, \dots, t_0^{63}] \uparrow$$



$$p_{t_1} = [t_1^0, t_1^1, \dots, t_1^{16}, t_1^{17}, \dots, t_1^{63}] \uparrow$$


...


$$p_{t_n} = [t_n^0, t_n^1, \dots, t_n^{16}, t_n^{17}, \dots, t_n^{63}] \uparrow$$


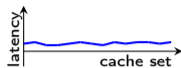
$$k_{t_{00}} = [0, 15, \dots, -1, -1, \dots, 0] \uparrow$$


$$k_{t_{01}} = [0, -1, \dots, -1, 15, \dots, 0] \uparrow$$


$$k_{t_{02}} = [0, -1, \dots, -1, -1, \dots, 0] \uparrow$$


...

$$k_{t_{FF}} = [0, -1, \dots, 15, -1, \dots, 0] \uparrow$$




$$\tilde{p}_{t_{0,00}} = p_{t_0} \odot k_{t_0}$$

$$\tilde{p}_{t_{1,00}} = p_{t_1} \odot k_{t_0}$$

...

$$\tilde{p}_{t_{n,00}} = p_{t_n} \odot k_{t_0}$$

$$\tilde{p}_{t_{0,01}} = p_{t_0} \odot k_{t_1}$$


$$\tilde{p}_{t_{1,01}} = p_{t_1} \odot k_{t_1}$$

...

...

...

$$\tilde{p}_{t_{n,FF}} = p_{t_n} \odot k_{t_{FF}}$$

$$\tilde{p}_{\mu_0} = \text{sum}(\tilde{p}_{t_0, \cdot})$$


$$\rho_0 = \sum \tilde{p}_{\mu_0}$$


...

$$\tilde{p}_{\mu_1} = \text{sum}(\tilde{p}_{t_1, \cdot})$$

$$\rho_1 = \sum \tilde{p}_{\mu_1}$$

...

$$\tilde{p}_{\mu_{FF}} = \text{sum}(\tilde{p}_{t_{FF}, \cdot})$$

$$\rho_{15} = \sum \tilde{p}_{\mu_{FF}}$$

Prime + Probe: attack final-round AES

$$p_{t_0} = [t_0^0, t_0^1, \dots, t_0^{16}, t_0^{17}, \dots, t_0^{63}] \uparrow \text{latency}$$

$$p_{t_1} = [t_1^0, t_1^1, \dots, t_1^{16}, t_1^{17}, \dots, t_1^{63}] \uparrow \text{latency}$$

...

$$p_{t_n} = [t_n^0, t_n^1, \dots, t_n^{16}, t_n^{17}, \dots, t_n^{63}] \uparrow \text{latency}$$

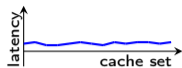
$$k_{t_{00}} = [0, 15, \dots, -1, -1, \dots, 0] \uparrow \text{latency}$$

$$k_{t_{01}} = [0, -1, \dots, -1, 15, \dots, 0] \uparrow \text{latency}$$

$$k_{t_{02}} = [0, -1, \dots, -1, -1, \dots, 0] \uparrow \text{latency}$$

...

$$k_{t_{FF}} = [0, -1, \dots, 15, -1, \dots, 0] \uparrow \text{latency}$$



$$\tilde{p}_{t_{0,00}} = p_{t_0} \odot k_{t_0}$$

$$\tilde{p}_{t_{1,00}} = p_{t_1} \odot k_{t_0}$$

...

$$\tilde{p}_{t_{n,00}} = p_{t_n} \odot k_{t_0}$$

$$\tilde{p}_{t_{0,01}} = p_{t_0} \odot k_{t_1}$$

$$\tilde{p}_{t_{1,01}} = p_{t_1} \odot k_{t_1}$$

...

...

...

$$\tilde{p}_{t_{n,FF}} = p_{t_n} \odot k_{t_{FF}}$$



$$\tilde{p}_{\mu_0} = \text{sum}(\tilde{p}_{t_{0,.}})$$

$$\rho_0 = \sum \tilde{p}_{\mu_0}$$

$$\tilde{p}_{\mu_1} = \text{sum}(\tilde{p}_{t_{1,.}})$$

$$\rho_1 = \sum \tilde{p}_{\mu_1}$$

...

$$\tilde{p}_{\mu_{FF}} = \text{sum}(\tilde{p}_{t_{FF,.}})$$

$$\rho_{15} = \sum \tilde{p}_{\mu_{FF}}$$

Prime + Probe: pros & cons

- + Work on non-shared memory

Prime + Probe: pros & cons

- + Work on non-shared memory
- Resolution of cache set
- Suffer from false positive/negative

Comparison

Flush+Reload

more accurate

require shared memory

identify cache line

Prime+Probe

more generic

fewer prerequisites

identify cache set

Other Variants

- Evict + Time [OST05]
- Evict + Reload [GSM15]
- Flush + Flush [GMWM16]
- Prime + Abort [DKPT17]
- Prime + Scope [PTV21]
- Prefetch + Prefetch [GZZY22]
- etc.

Summary

- Mastik: toolkit to collect cache timing side-channel information
- General steps in cache attacks:
 1. Prepare cache
 2. (victim executes)
 3. Get timing difference

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- General steps in cache attacks:

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3. Get timing difference

Flush + Reload

clear cache line

reload cache line

Summary

- Mastik: toolkit to collect cache timing side-channel information
- General steps in cache attacks:

	<u>Flush + Reload</u>	<u>Prime + Probe</u>
1. Prepare cache	clear cache line	fill cache with own data
2. (victim executes)		
3. Get timing difference	reload cache line	re-access own data